



"RESPONSE UNDER 37 CFR 1.116-
EXPEDITED PROCEDURE EXAMINING
GROUP 2822"

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Docket No.: 240800US2S DIV

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

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BOX AF

RE: Application Serial No.: 10/670,529

Applicants: Norihisa ARAI

Filing Date: September 26, 2003

For: METHOD OF MANUFACTURING
SEMICONDUCTOR DEVICE INCLUDING
PROCESS FOR IMPLANTING IMPURITIES INTO
SUBSTRATE VIA MOS TRANSISTOR GATE
ELECTRODE AND GATE INSULATION FILM

Group Art Unit: 2822

Examiner: Rose, K. L.

SIR:

Attached hereto for filing are the following papers:

Request for Reconsideration Under 37 C.F.R. § 1.116

Our check in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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"RESPONSE UNDER 37 CFR 1.116-
EXPEDITED PROCEDURE EXAMINING
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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :
NORIHISA ARAI : EXAMINER: ROSE, K. L.
SERIAL NO: 10/670,529 :
FILED: SEPTEMBER 26, 2003 : GROUP ART UNIT: 2822
FOR: METHOD OF MANUFACTURING :
SEMICONDUCTOR DEVICE INCLUDING
PROCESS FOR IMPLANTING
IMPURITIES INTO SUBSTRATE VIA
MOS TRANSISTOR GATE ELECTRODE
AND GATE INSULATION FILM

REQUEST FOR RECONSIDERATION UNDER 37 C.F.R. § 1.116

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

This Request for Reconsideration is in response to the Office Action dated April 4,
2005.

Remarks/Arguments begin on page 2 of this paper.